



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER OF PATENTS AND TRADEMARKS  
Washington, D.C. 20231  
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/612,298	07/07/2000	Tatsuya Kunikiyo	193414US2	8554

22850 7590 12/18/2002

OBLON SPIVAK MCCLELLAND MAIER & NEUSTADT PC  
FOURTH FLOOR  
1755 JEFFERSON DAVIS HIGHWAY  
ARLINGTON, VA 22202

[REDACTED] EXAMINER

CAO, PHAT X

ART UNIT	PAPER NUMBER
2814	

DATE MAILED: 12/18/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

09/612,298

Applicant(s)

KUNIKIYO, TATSUYA

Examiner

Phat X. Cao

Art Unit

2814

-- The MAILING DATE of this communication appears on the cover sheet with the corresponding address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

1) Responsive to communication(s) filed on 01 November 2002.

2a) This action is FINAL. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

4) Claim(s) 1,7-15 and 21-23 is/are pending in the application.

4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5) Claim(s) 10-12 and 21-23 is/are allowed.

6) Claim(s) 1,7-9 and 13-15 is/are rejected.

7) Claim(s) \_\_\_\_\_ is/are objected to.

8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on 01 October 2002 is/are: a) accepted or b) objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11) The proposed drawing correction filed on \_\_\_\_\_ is: a) approved b) disapproved by the Examiner.  
If approved, corrected drawings are required in reply to this Office action.

12) The oath or declaration is objected to by the Examiner.

### Priority under 35 U.S.C. §§ 119 and 120

13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) All b) Some \* c) None of:  
1. Certified copies of the priority documents have been received.  
2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).  
\* See the attached detailed Office action for a list of the certified copies not received.

14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).  
a) The translation of the foreign language provisional application has been received.

15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

### Attachment(s)

1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_.  
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) Notice of Informal Patent Application (PTO-152)  
3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_\_. 6) Other: \_\_\_\_\_

Art Unit: 2814

## DETAILED ACTION

### *Claim Rejections - 35 USC § 103*

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 1, 7-9, and 13-15 are rejected under 35 U.S.C. 103(a) as being unpatentable over JP. 10-199882 in view of Lee (US. 5,441,915).

With respect to claims 1 and 7-9, JP ('882), in Fig. 2 and abstract, discloses a semiconductor device comprising: a semiconductor substrate 1 having a main surface along which a semiconductor element is formed; interlayer insulating films formed on the main surface; conductive interconnections 3, 5, 7, and 9 provided in a plurality of layers separated by the interlayer insulating films; conductive dummy interconnections 3A, 5A, 7A, 9A, and 12 provided in the plurality of layers so that each conductive dummy interconnection is formed in a layer of the plurality of layers with at least one conductive interconnection, wherein the dummy interconnections are provided to sandwich an interconnection part included in the interconnections in at least one of the two or more layers and wherein the dummy interconnections also comprise a dummy interconnection 12 which is provided in a layer located over the at least one layer to cover the interconnection part; and a conductive dummy plug 4b, 6b,

Art Unit: 2814

8b, and 10b selectively buried in the interlayer insulating films to connect the dummy interconnections all together.

JP ('882) does not disclose the dummy interconnections connected to a potential power supply line.

However, Lee, in column 5, lines 57-65, teaches the obviousness of having the dummy lines left floating or grounded depending on their locations. Accordingly, it would have been obvious to connect the dummy lines of JP ('882) to the ground line because according to Lee, the dummy lines would be grounded when the dummy lines are adjacent to signal lines (see column 5, lines 62-65) for the known purpose of preventing cross-talk.

With respect to claims 13-14, JP ('882) also discloses in Fig. 3 the forming of a passivation film 11 covering the uppermost layer and having a higher thermal conductivity than the interlayer insulating films, and the forming of a heat sink 12 which is in contact with the passivation film 11.

With respect to claim 15, in view of teachings of Fig. 2 of JP ('882), it would have been obvious to modify Fig. 3 of JP ('882) by forming another conductive dummy plug (see dummy plugs 10b in Fig. 2) buried in the passivation film to connect the heat sink 12 and part of the dummy interconnection for the purpose of performing heat dissipation more efficiently (see abstract).

3. Claims 1, 7-9, and 13-15 are rejected under 35 U.S.C. 103(a) as being unpatentable over JP. 10-199882 in view of Ma (US. 5,729,047).

Art Unit: 2814

As discussed in details above, JP ('882) substantially reads on the invention as claimed, except it does not disclose the dummy interconnections connected to a potential power supply line.

However, Ma, in Fig. 3, teaches the obviousness of forming the dummy interconnections surrounding the signal interconnection line 90 and connecting to the potential power supply line Vss. Accordingly, it would have been obvious to connect the dummy interconnections of JP ('882) to the power supply line Vss because according to Ma, such modification would provide a signal isolation and decoupling in an integrated circuit device (column 1, lines 55-60).

***Allowable Subject Matter***

4. Claims 10-12 and 21-23 are allowed.

***Response to Arguments***

5. With respect to JP('882), Applicant argues that JP('882) does not disclose "each conductive dummy interconnection is formed in a layer of said plurality of layers with at least one conductive interconnection" as amended.

The Examiner disagrees because Fig. 2 of JP('882) clearly discloses the above feature.

With respect to the combination of JP 10-199882 with either Lee or Ma, Applicant argues that it is not obvious to combine them because neither Lee nor Ma teaches the arrangement of a

Art Unit: 2814

Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (703) 308-0956. Group 2800 fax number is (703) 308-7722 or (703) 308-7724.

PC

November 29, 2002

*Caomuon*  
PHAT X. CAO  
PRIMARY EXAMINER



Creation date: 09-10-2003

Indexing Officer: AGOMEZ - ALFREDO GOMEZ, JR.

Team: OIPEBackFileIndexing

Dossier: 09612298

Legal Date: 03-13-2003

No.	Doccode	Number of pages
1	A...	1
2	REM	4
3	LET.	1

Total number of pages: 6

Remarks:

Order of re-scan issued on .....